



FOOTPRINT FOR GS665XX-EVBDB

EXTERNAL PWM INPUT TO DSP/MCU CONTROL BOARD

ON BOARD DEAD TIME GENERATION CIRCUIT
USE TR1 AND TR2 TO ADJUST DEAD TIME

PWM OUTPUT
INVERTED PWM OUTPUT

PWM INPUT SELECTION
POS 1: EXT. PWM SIGNAL
POS 2/3: INTERNAL PWM SIGNAL
POS 4: OFF

- NOTES - UNLESS OTHERWISE SPECIFIED
1. ALL SMD RESISTORS AND CAPACITORS ARE 0603 SIZE
 2. DO NOT INSTALL TR1, TR2, R2, R3 AND C12

M1 M2 M3 M4
PCB STANDOFF 4.75MM MNT HOLE
KEYSTONE 8839-8834

Title 650V GAN UNIVERSAL MOTHERBOARD		GaN Systems
Sheet Description		
Document GS665EVBMB	Sheet 1 of 1	1145 Innovation Dr. Suite 101 Ottawa ON K2K 3G8 This document contains information proprietary to GaN Systems.
Date: Tuesday, June 28, 2016		